

Electronic Acknowledgement Receipt

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Application Number:	10709048
International Application Number:	
Confirmation Number:	3047
Title of Invention:	A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE SPACER CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	FIS920030397US1_Amendment.pdf	49298	no	12
			24590fed1a821df60563137a781e573e2caa9a9e		
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	FIS920030397US1_RCE.pdf	116040	no	1
			09c249e3b6f862cfae1992ba617c08a092df1c12		
Warnings:					
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Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8242	no	2
			6d564a64ffb1f83df51290c2c53a770b266e2ecf		
Warnings:					
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Total Files Size (in bytes):			173580		
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